

FIG. 1

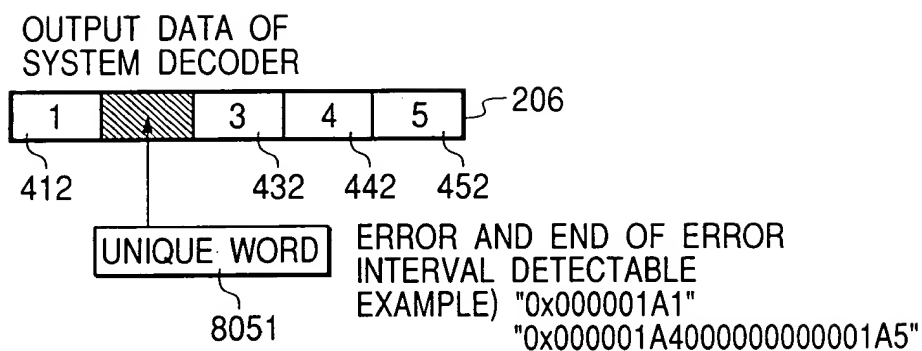
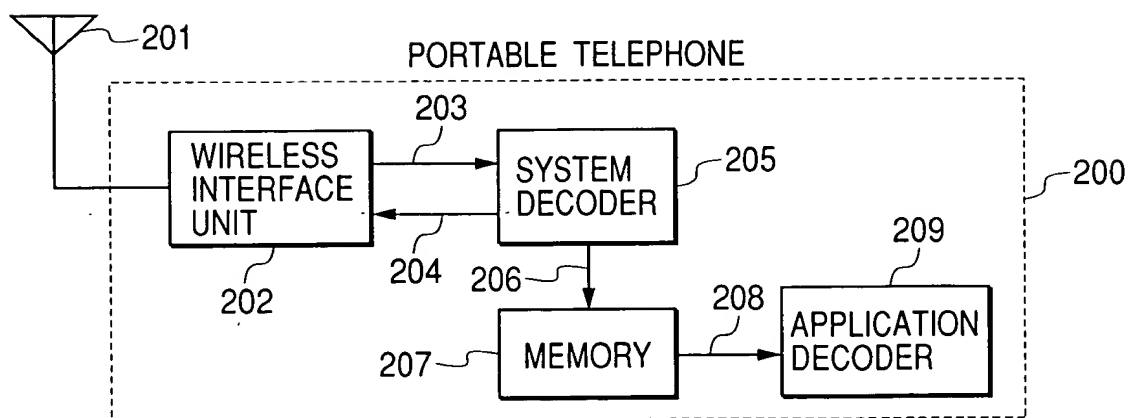


FIG. 2



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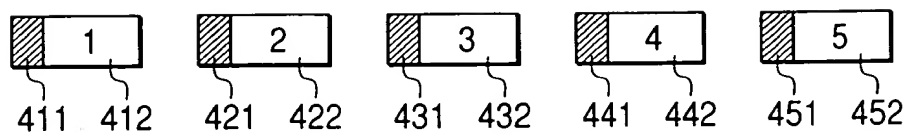
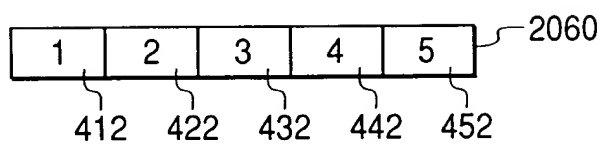
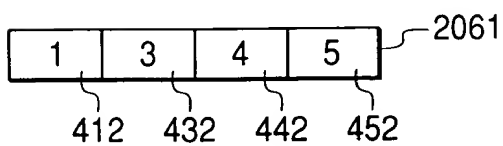
*FIG. 3**FIG. 4**FIG. 5*

FIG. 6

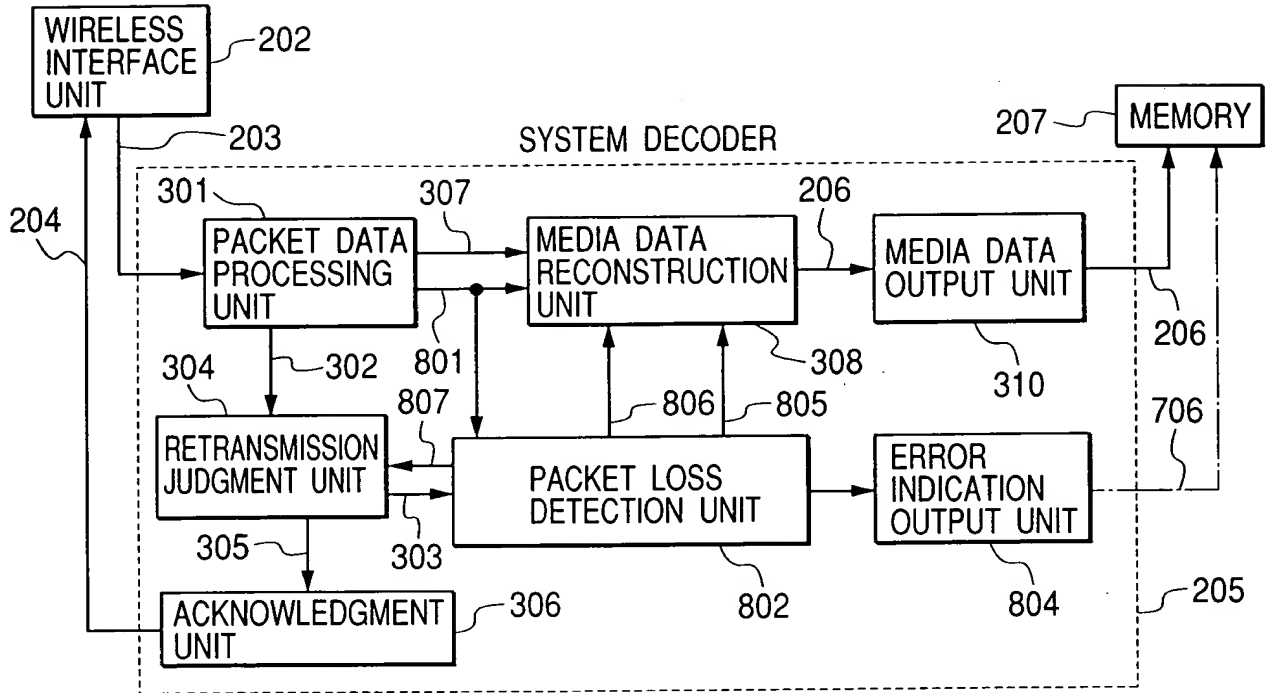


FIG. 7(a)

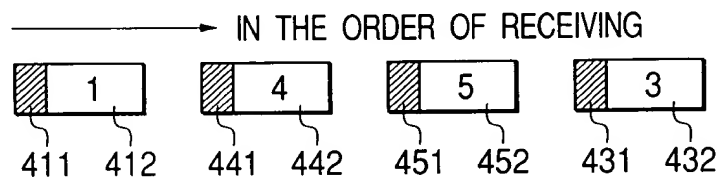


FIG. 7(b)

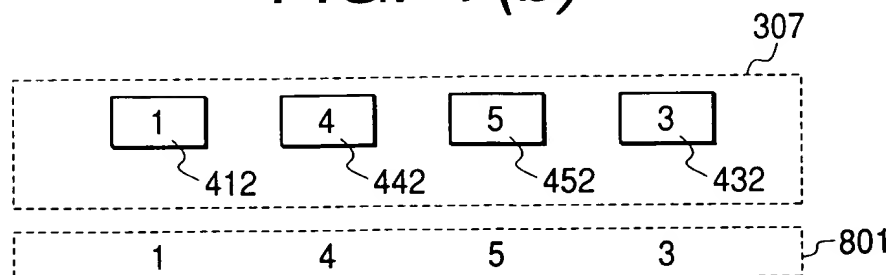
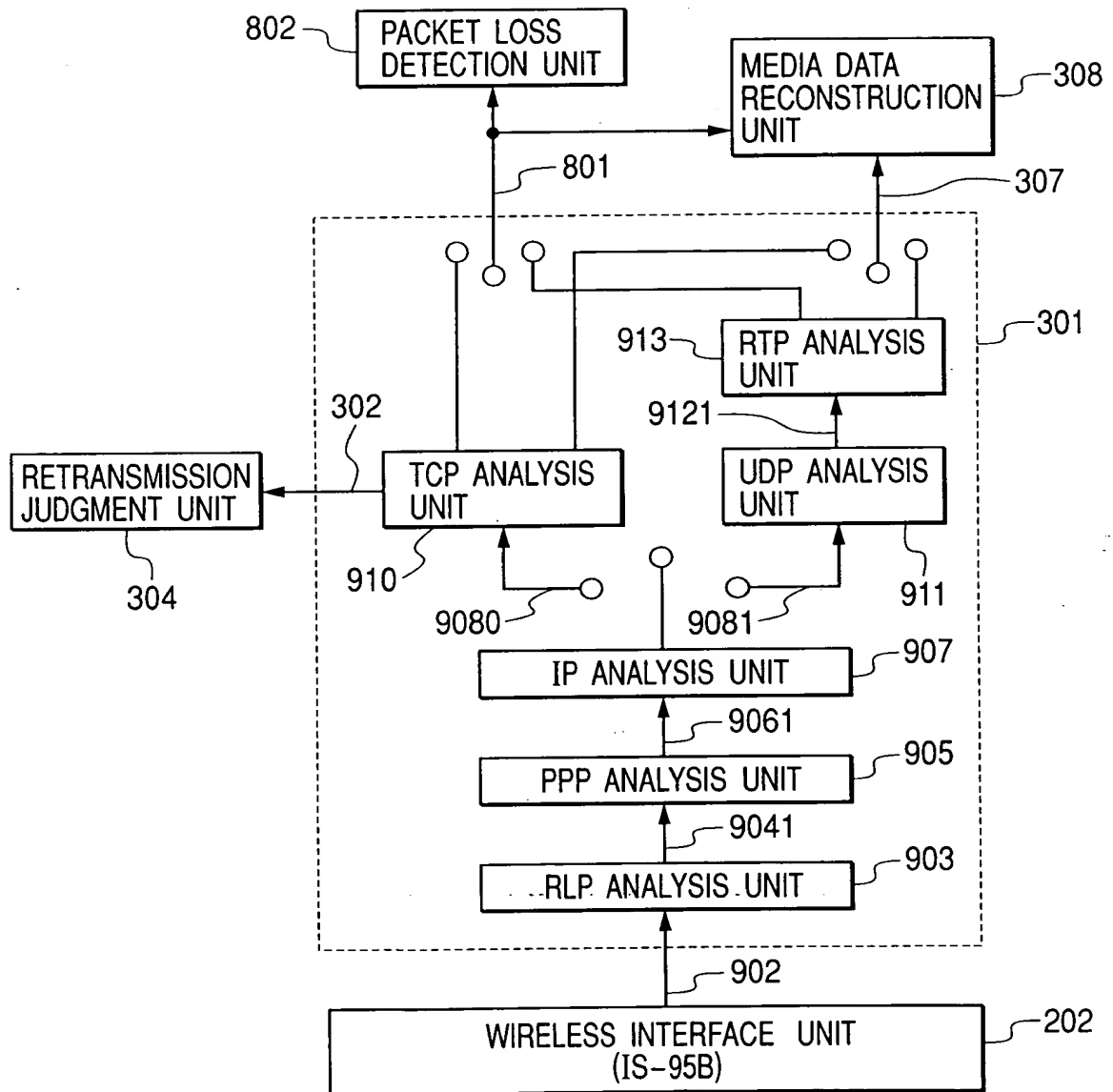


FIG. 8



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**FIG. 9**

## IPv4 PACKET

VERSION (4 BITS)	HEADER LENGTH (4 BITS)	PRIORITY (3 BITS)	SERVICE TYPE (5 BITS)	TOTAL IP LENGTH (2 BYTES)	
DATAGRAM ID (2 BYTES)	FRAGMENT (2 BYTES)	TIME TO LIVE (TTL) (1 BYTE)		PROTOCOL (1 BYTE)	CHECKSUM (2 BYTES)
SOURCE PORT ADDRESS (4 BYTES)	DESTINATION PORT ADDRESS (4 BYTES)	PAYLOAD (VARIABLE LENGTH)			

9061

908

**FIG. 10**

## PPP FRAME

FLAG (1 BYTE)	DESTINATION PORT ADDRESS (1 BYTE)	CONTROL (1 BYTE)	PROTOCOL (2 BYTES)	PAYLOAD (VARIABLE LENGTH)	CYCLIC REDUNDANCY CHECK (2 BYTES)	FLAG (1 BYTE)
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906

**FIG. 11**

## RLP FRAME

SEQUENCE NUMBER (1 BYTE)	TYPE OF FRAME (1 BIT)	PAYLOAD LENGTH (7 BITS)	PAYLOAD (VARIABLE)	PADDING (VARIABLE)
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904

FIG. 12

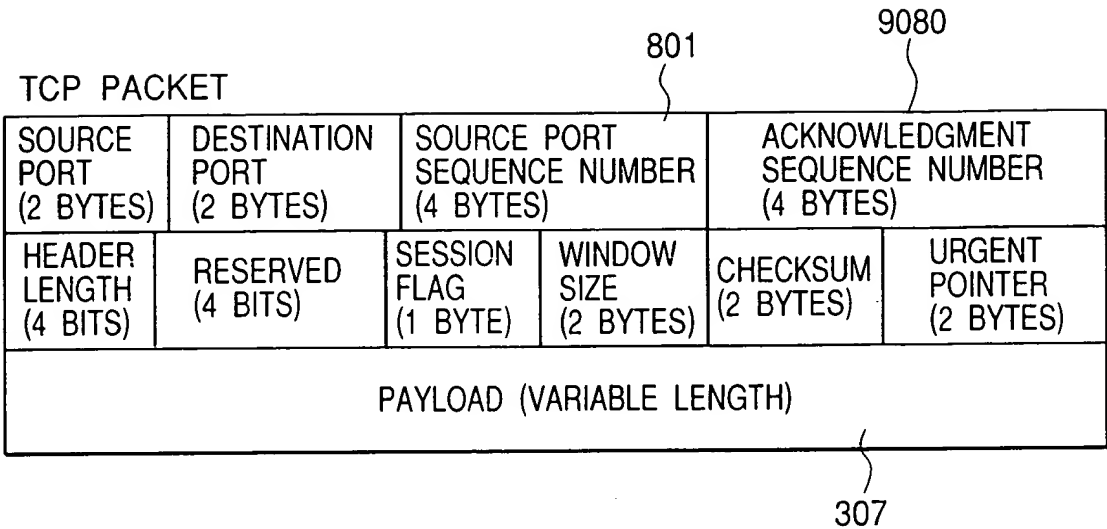


FIG. 13

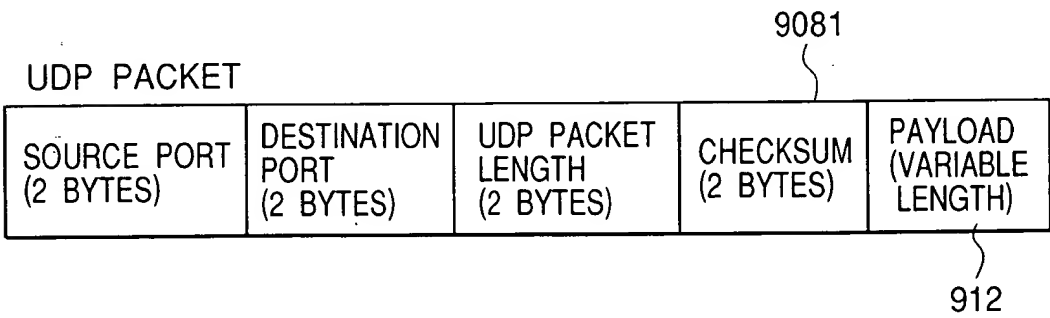
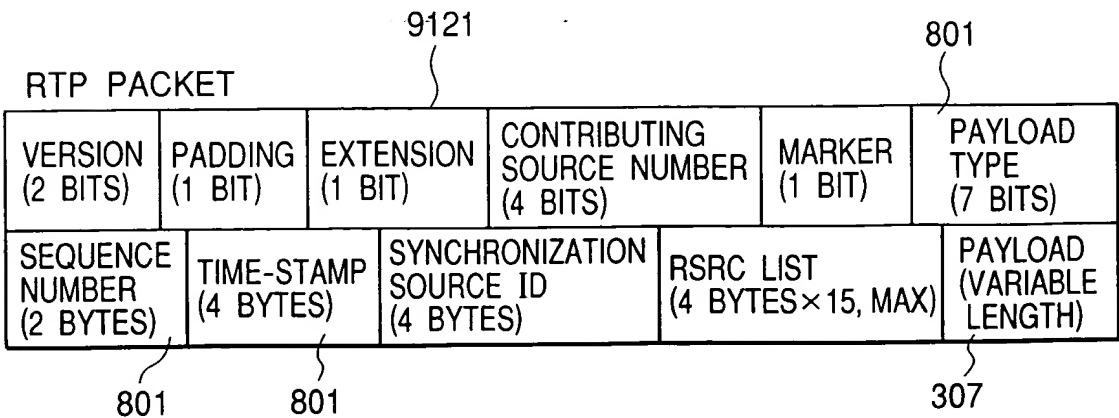
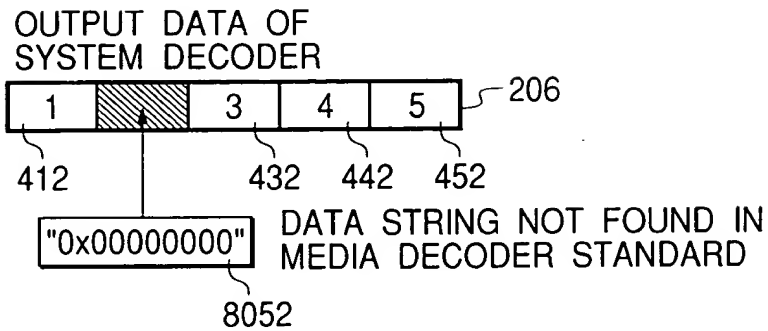
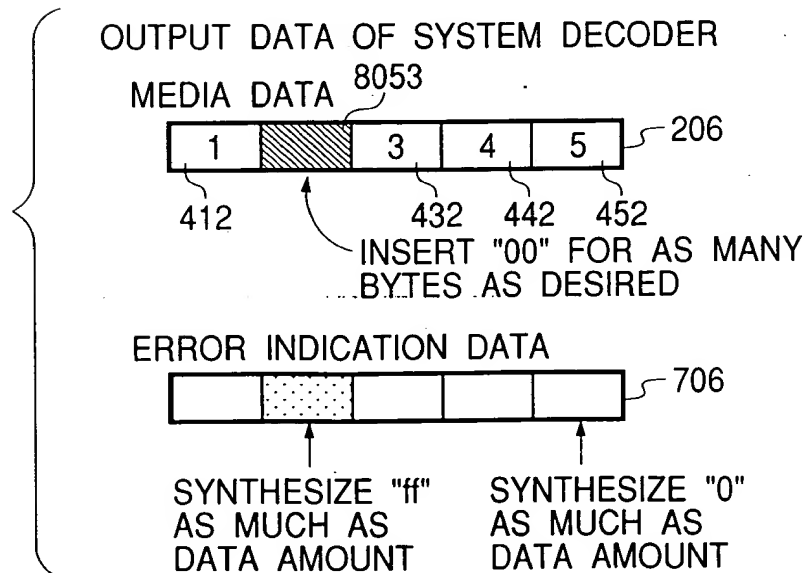


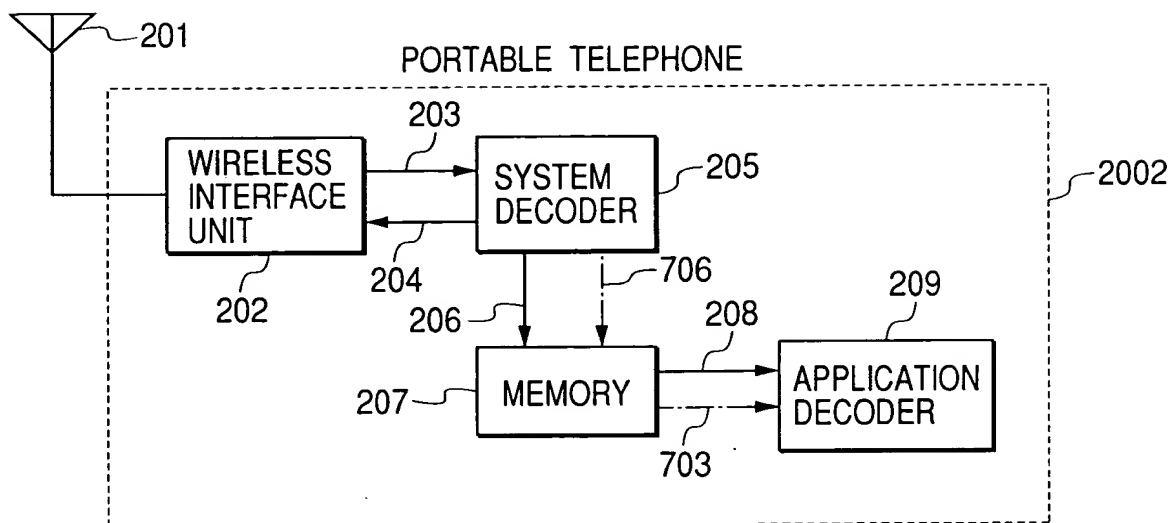
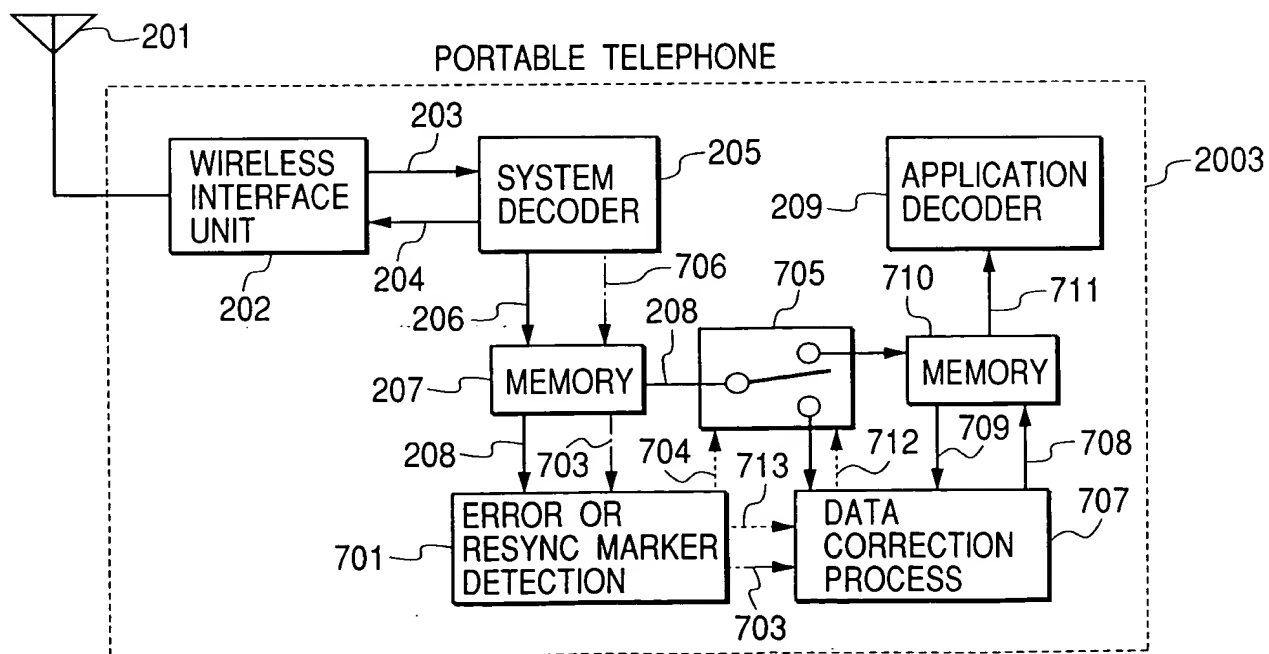
FIG. 14



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*FIG. 15**FIG. 16*

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**FIG. 17****FIG. 18**



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FIG. 19

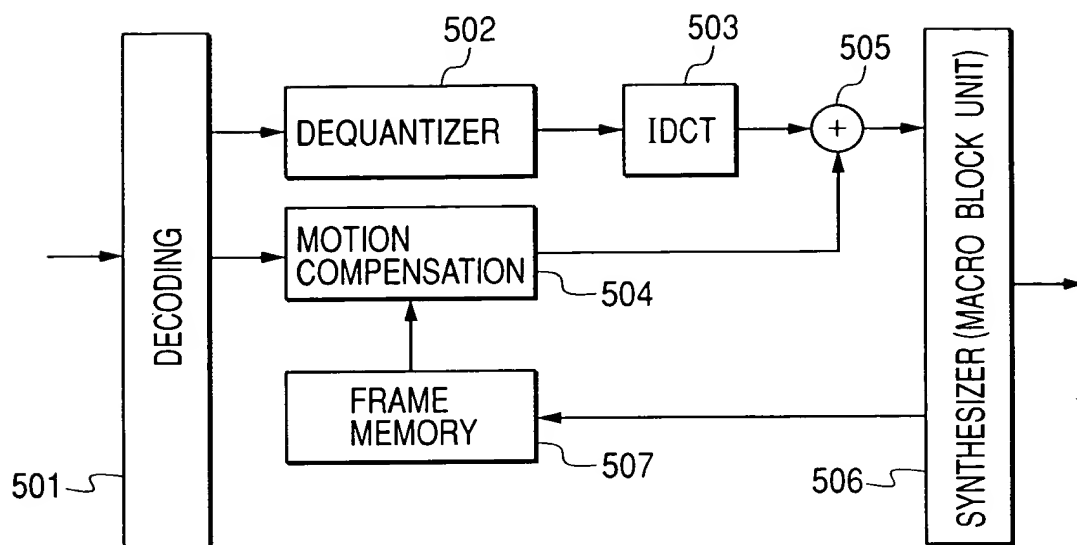


FIG. 20

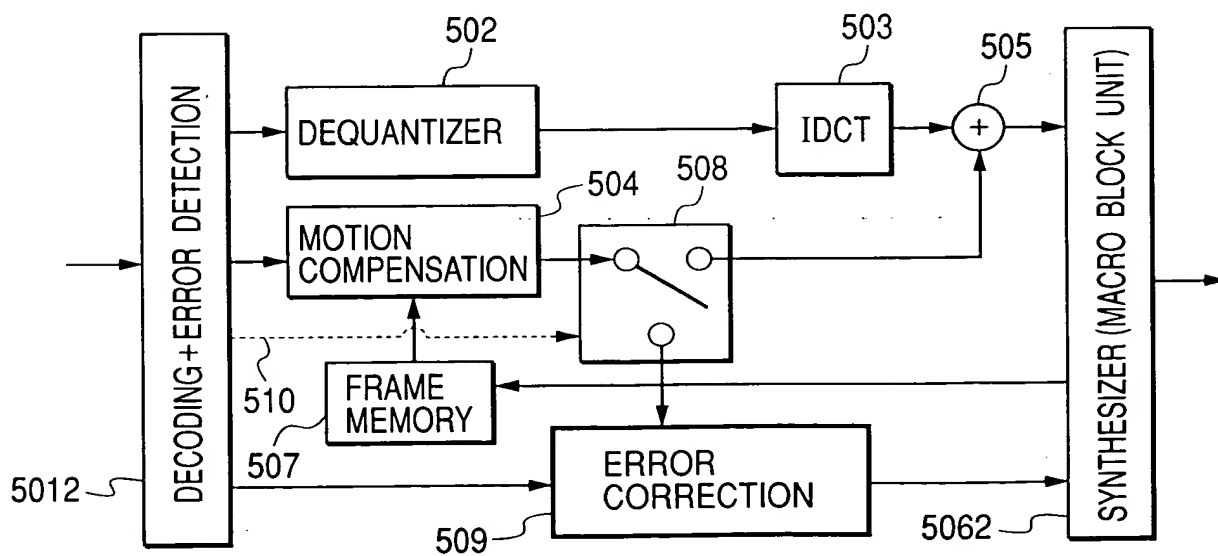


FIG. 21

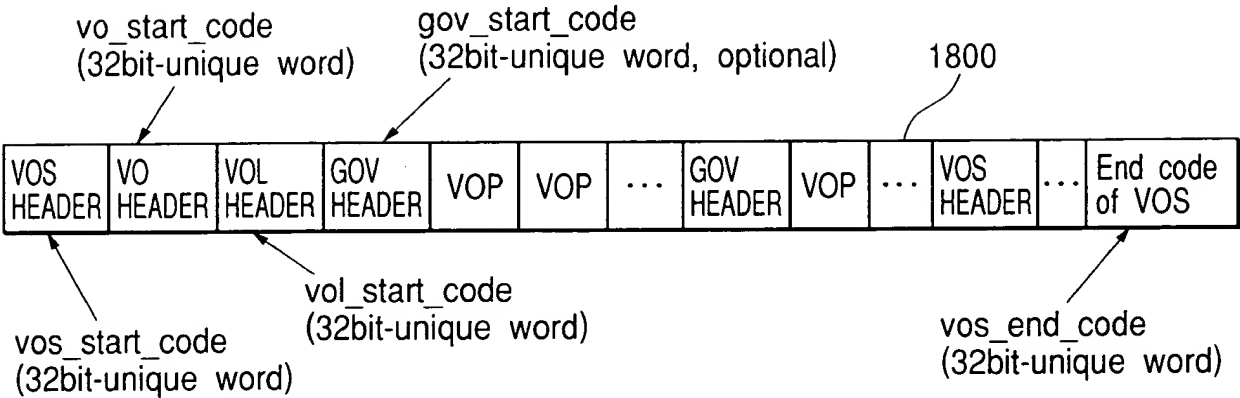


FIG. 22

vop_start_code (32bit-unique word)		vop_coding_type (2bit)		modulo_time_base (1bit AND ABOVE, TERMINATE WITH "0")		marker_bit (1bit)
vop_time_increment (1-16bit VARIABLE)		marker_bit (1bit)	vop_coded (1bit)	vop_rounding_type (IN CASE OF 1bit, vop_coding_type != "1")		
intra_dc_vlc_thr (3bit)	vop_quant (5bit)	vop_fcode_forward (IN CASE OF 3bit, vop_coding_type != "1")			vop_fcode_backward (IN CASE OF 3bit, vop_coding_type == "B")	

FIG. 23

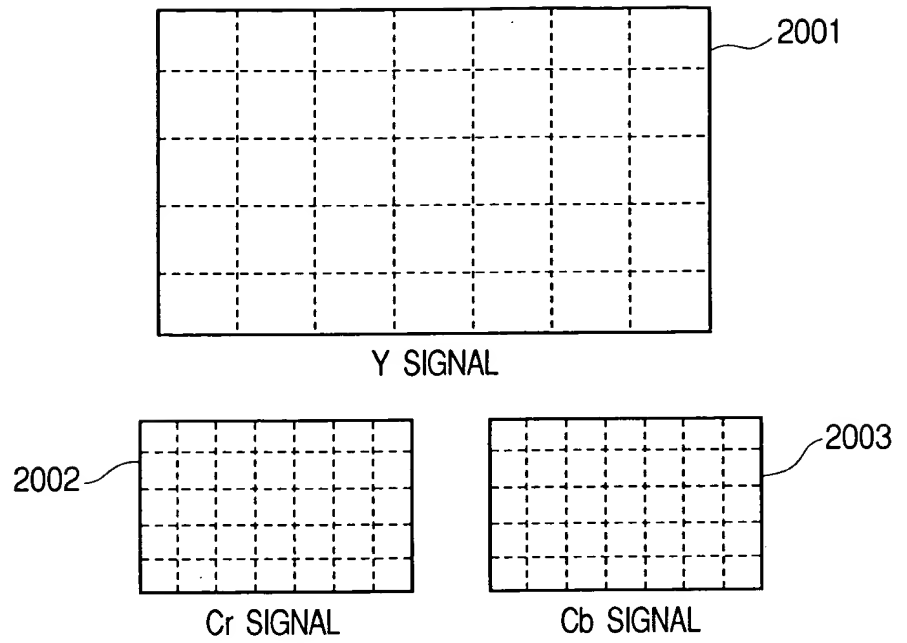
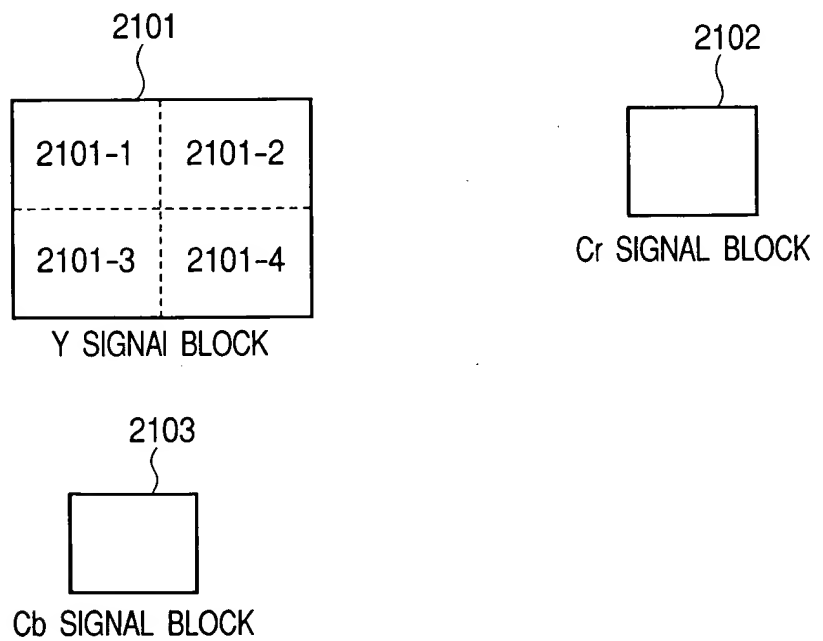


FIG. 24

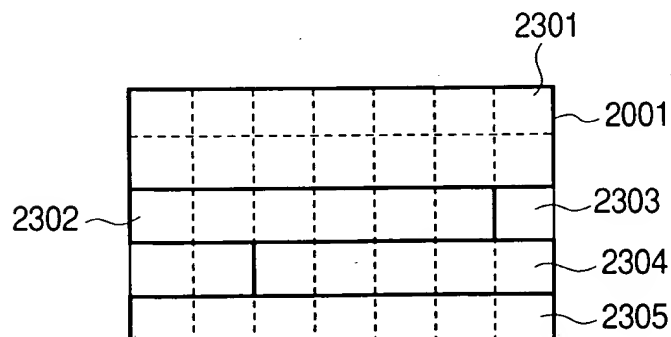


*FIG. 25*

2200

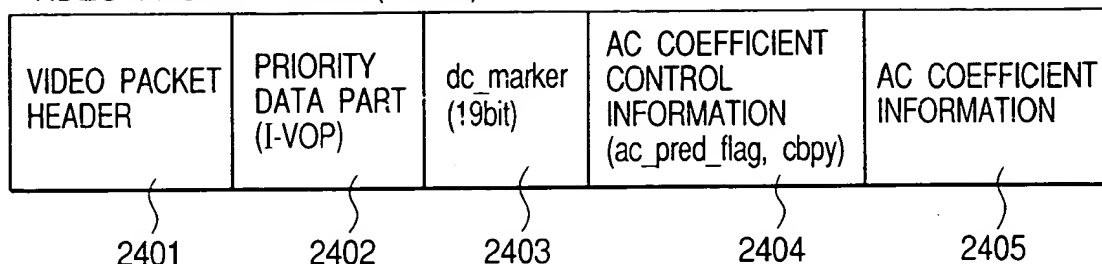
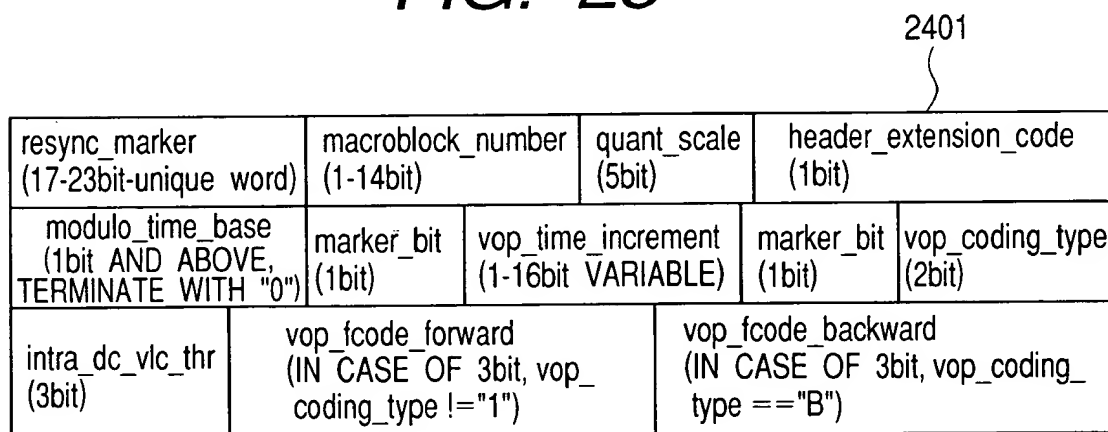
not_coded (1bit, vop_coding_type==IN CASE OF "p")	mcbpc (1-9bit VARIABLE)	ac_pred_flag (1bit, mb_type==IN CASE OF intra or intra+q)
cpby (1-6bit, mb_type!=IN CASE OF stuffing)	dquant (2bit, mb_type==IN CASE OF intra+q or inter+q)	MOTION VECTOR (mb_type==inter, inter+q or inter4v)
DIFFERENTIAL intra DC COEFFICIENT (mb_type==intra or intra+q AND use_intra_dc_vlc==IN CASE OF "1")		Intra AC COEFFICIENT or inter DC & AC COEFFICIENT (BLOCK DESIGNATED BY cpby, cbpci)

mcbpc : mb\_type (intra, intra+q, inter, inter+q, inter4v, stuffing), cbpc  
not\_coded : IN CASE OF "1", mb\_type=inter, NO MOTION, mcbpc  
AND THEREAFTER OMITTED  
use\_intra\_dc\_vlc : DETERMINE BY quant AND intra\_dc\_vlc\_thr,  
AND TAKE THE VALUE OF "0" OR "1"

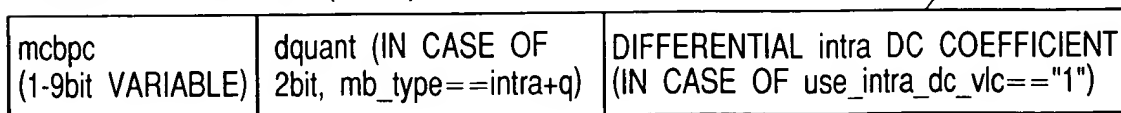
*FIG. 26*

**FIG. 27**

## VIDEO PACKET DATA (I-VOP)

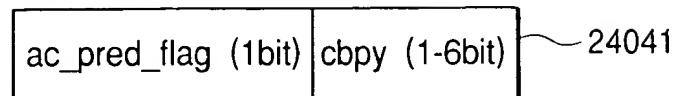
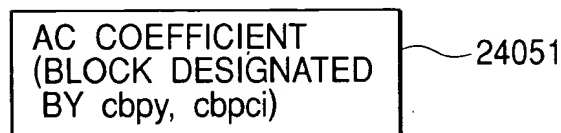
**FIG. 28****FIG. 29**

## PRIORITY DATA PART (I-VOP)



mcbpc : mb\_type (intra, intra+q stuffing), cbpc

use\_intra\_dc\_vlc : DETERMINE BY quant AND intra\_dc\_vlc\_thr,  
AND TAKE THE VALUE OF "0" OR "1"

*FIG. 30*AC COEFFICIENT  
CONTROL INFORMATION*FIG. 31*AC COEFFICIENT  
INFORMATION*FIG. 32*

VIDEO PACKET DATA (P-VOP)

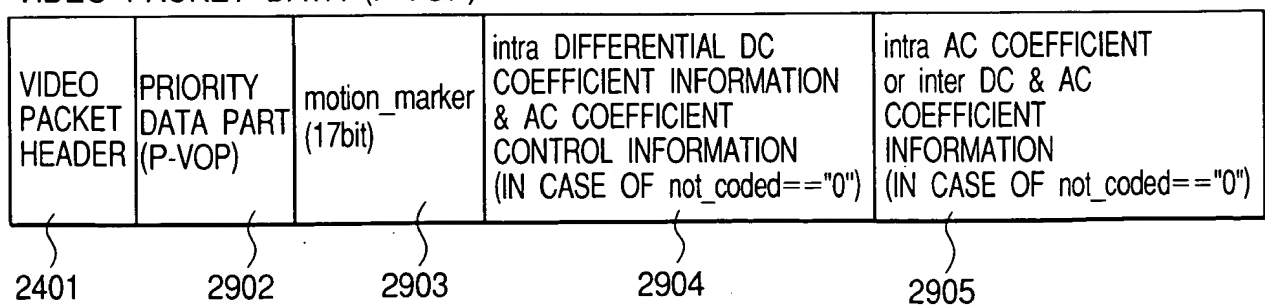


FIG. 33

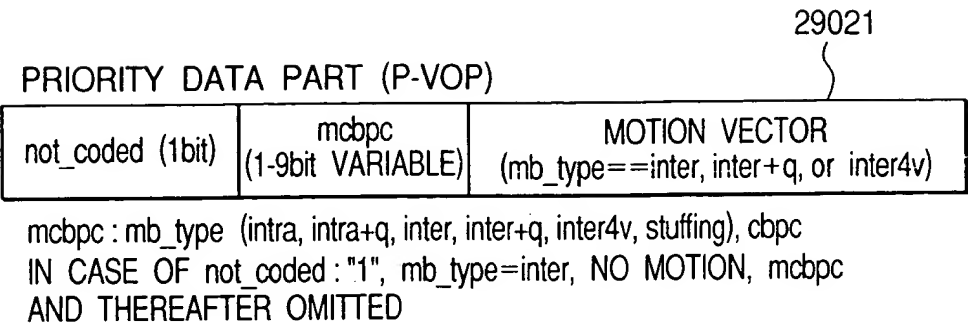


FIG. 34

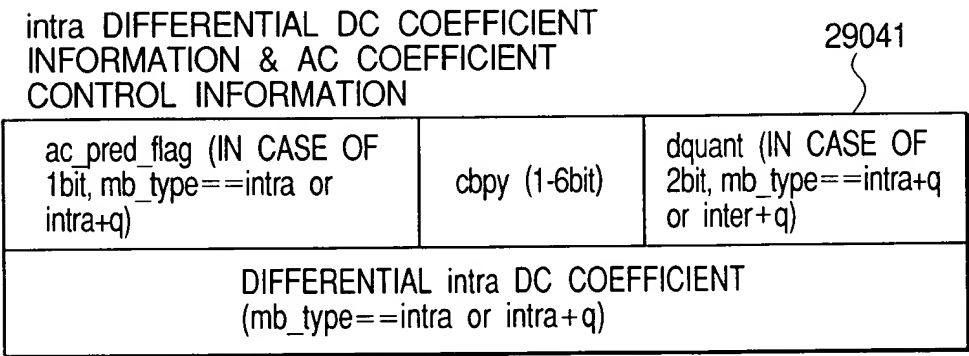
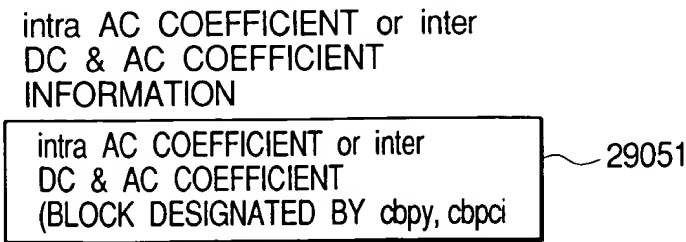


FIG. 35



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FIG. 36

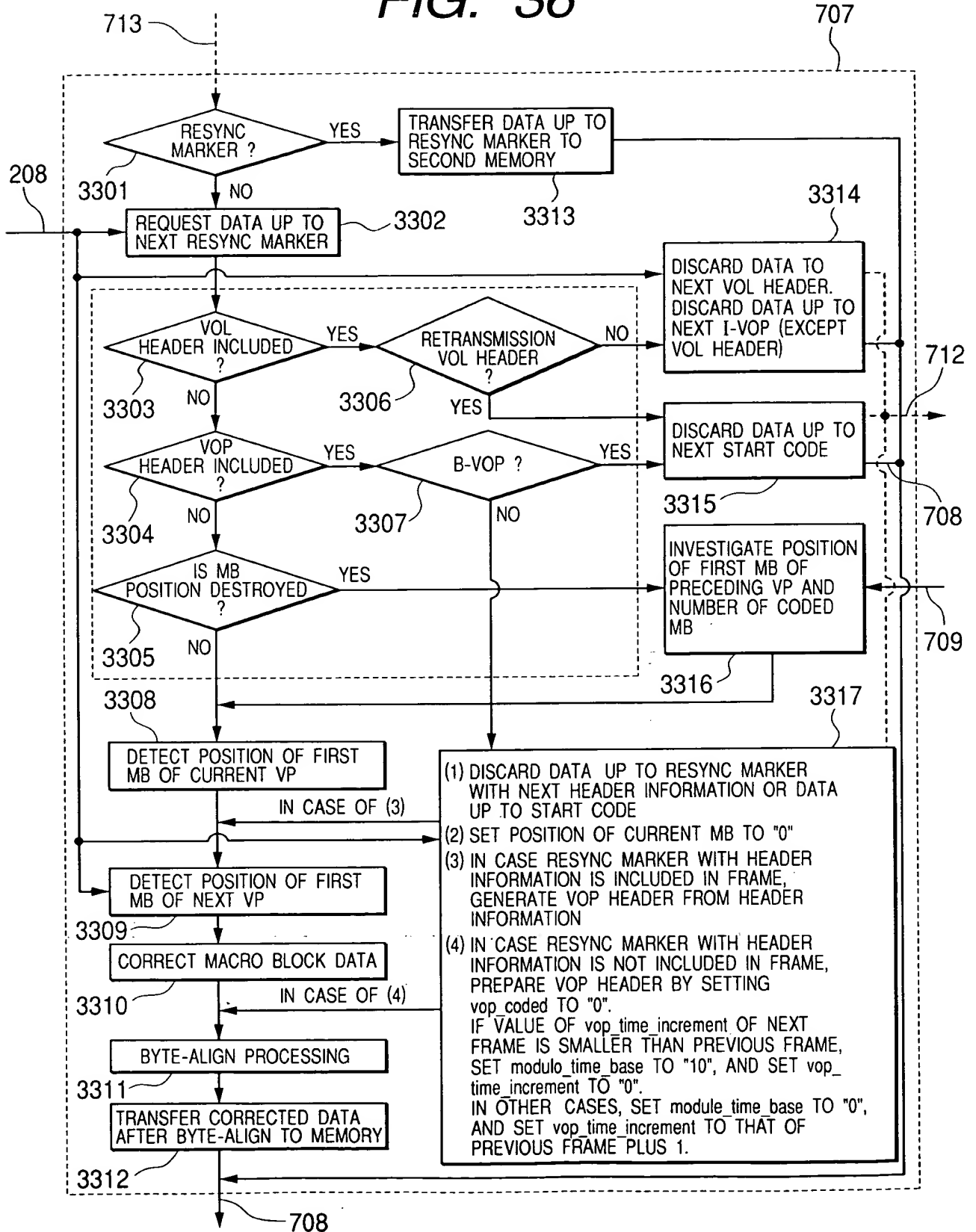
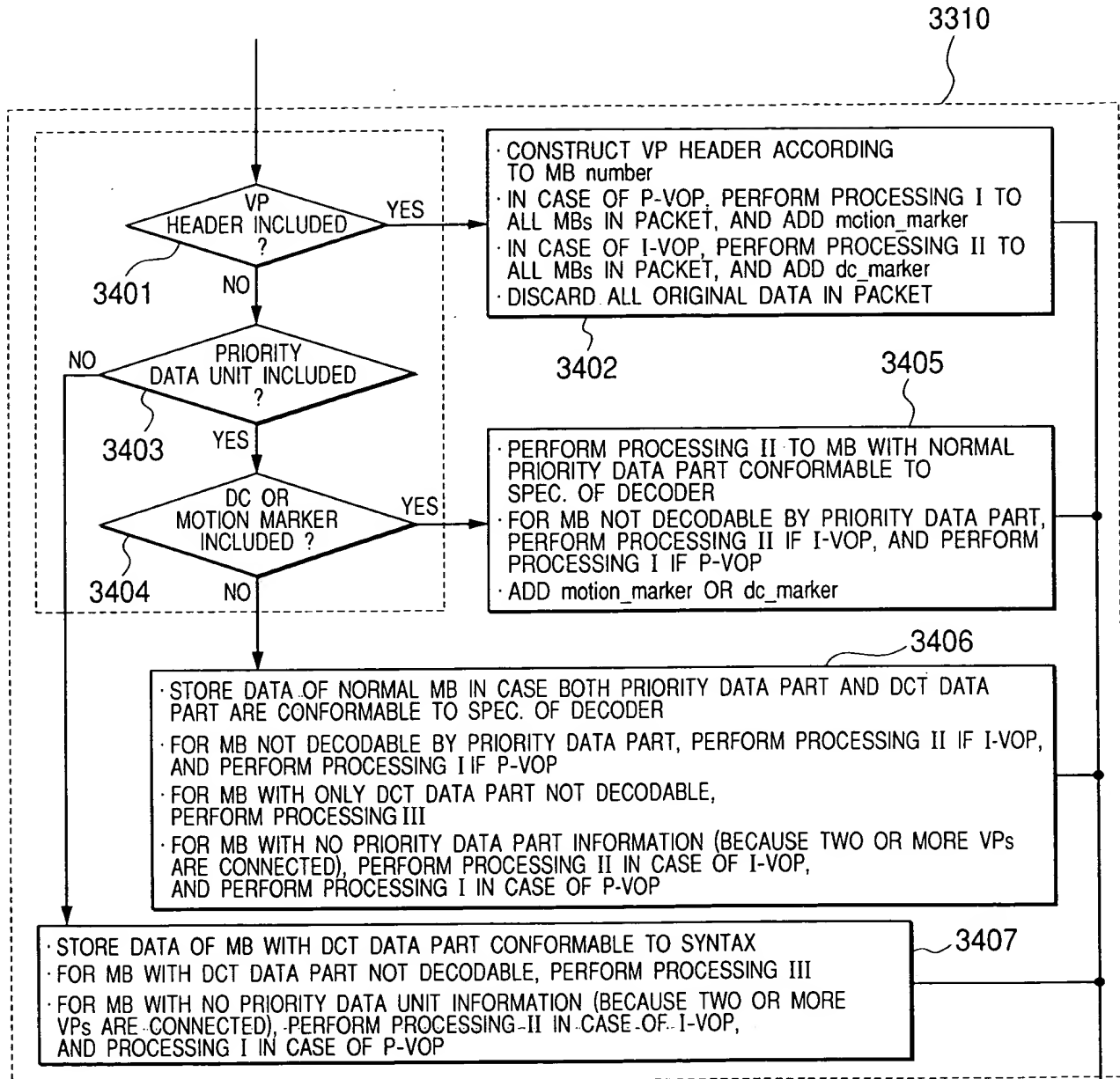




FIG. 37



PROCESSING I: SET not\_coded FLAG TO "1". DELETE ORIGINAL DATA IN MB.

PROCESSING II: SET ALL DIFFERENTIAL DC COEFFICIENTS IN MB TO "0" AND SET mb\_type TO "intra" AND SET cpy AND cbpc (mcbpc) TO NO CODED BLOCK.  
DELETE ORIGINAL DATA IN MB.

PROCESSING III: SET cpy AND cbpc (mcbpc) TO NO CODED BLOCK.  
FURTHER, IN CASE OF I-VOP, SET ac\_pred\_flag TO "0", AND DELETE AC COEFFICIENT DATA.  
IN CASE OF P-VOP, PERFORM PROCESSING I IF INTRA CODING. IF mb\_type IS PREDICTIVE CODING DELETE inter DC & AC COEFFICIENT DATA.

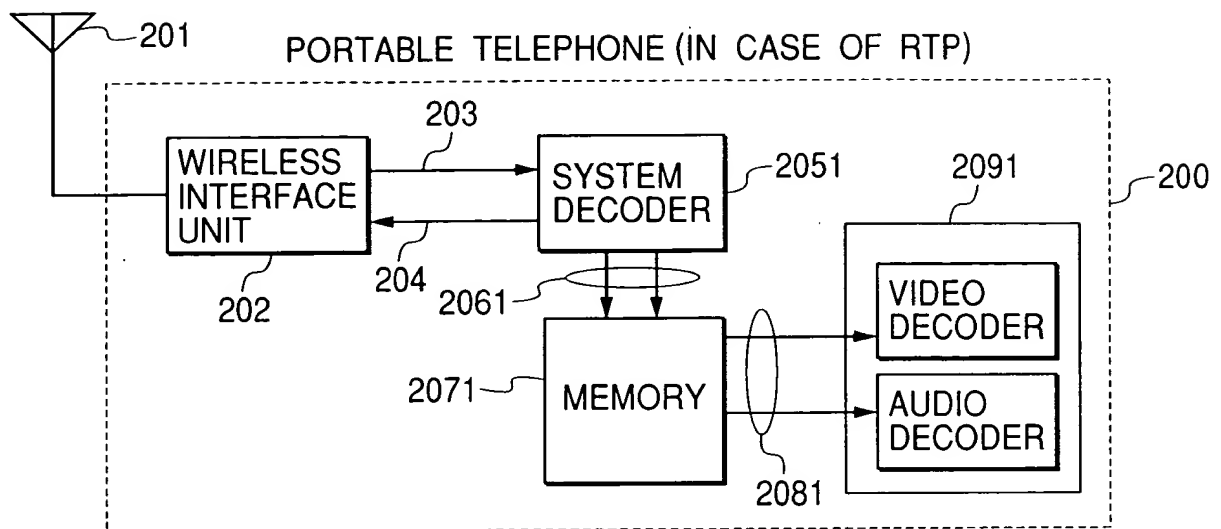
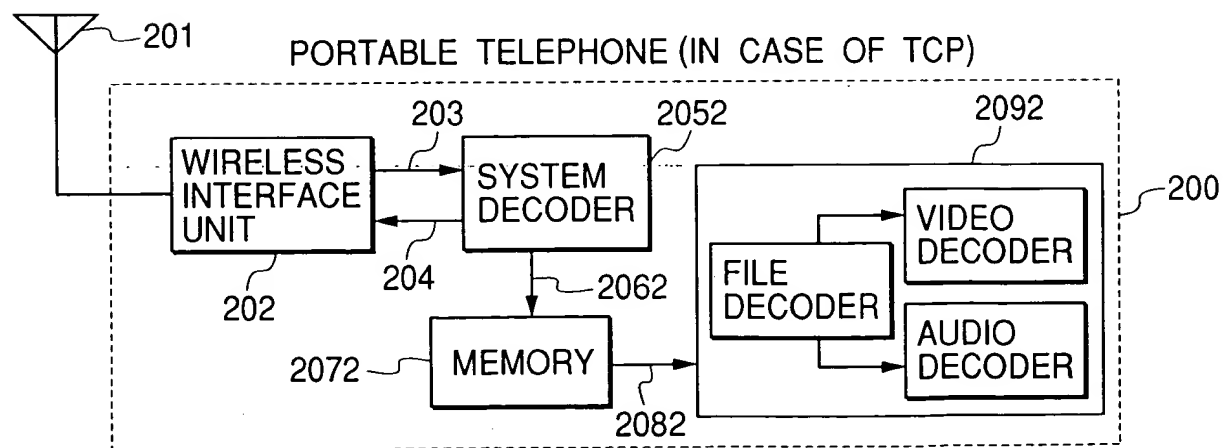
*FIG. 38**FIG. 39*

FIG. 40

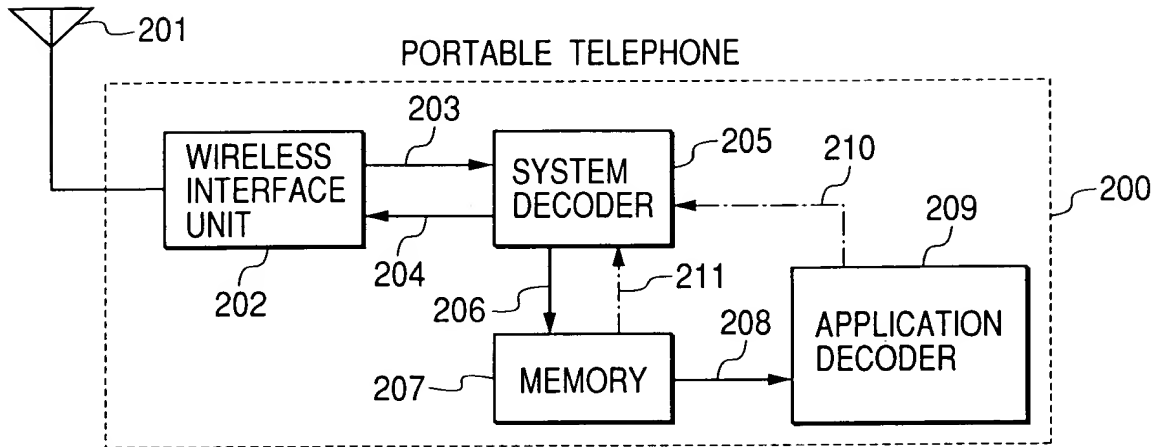


FIG. 41

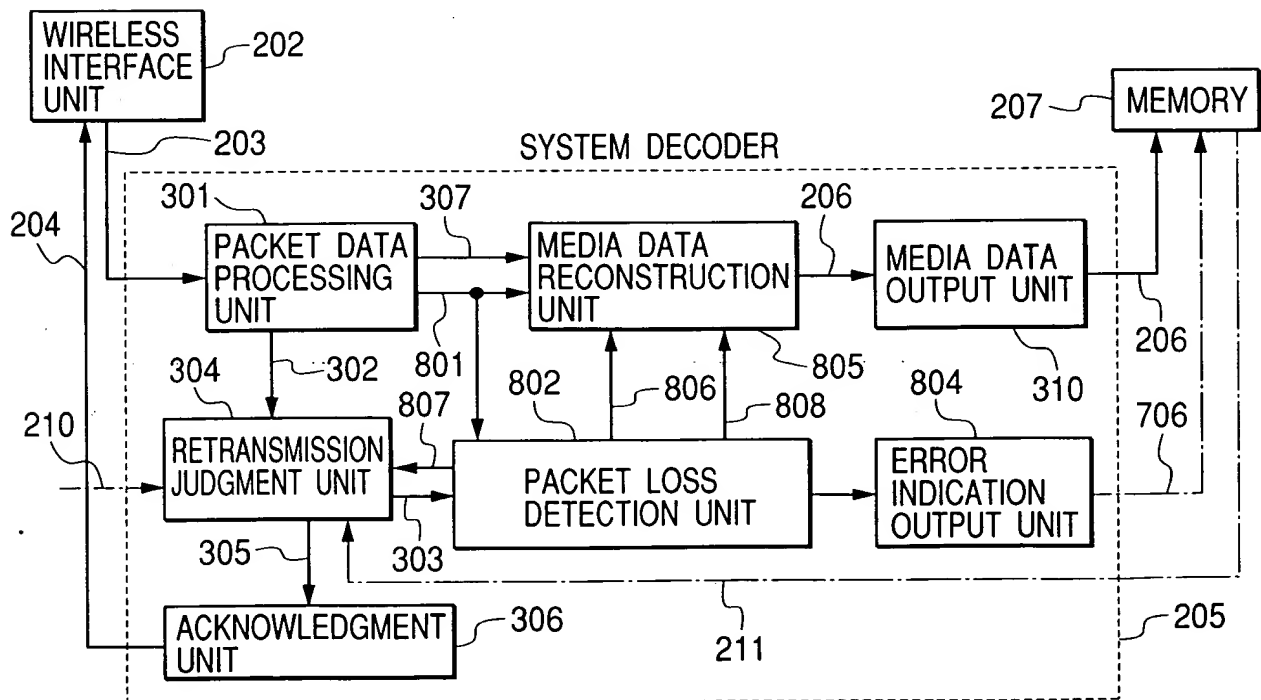


FIG. 42

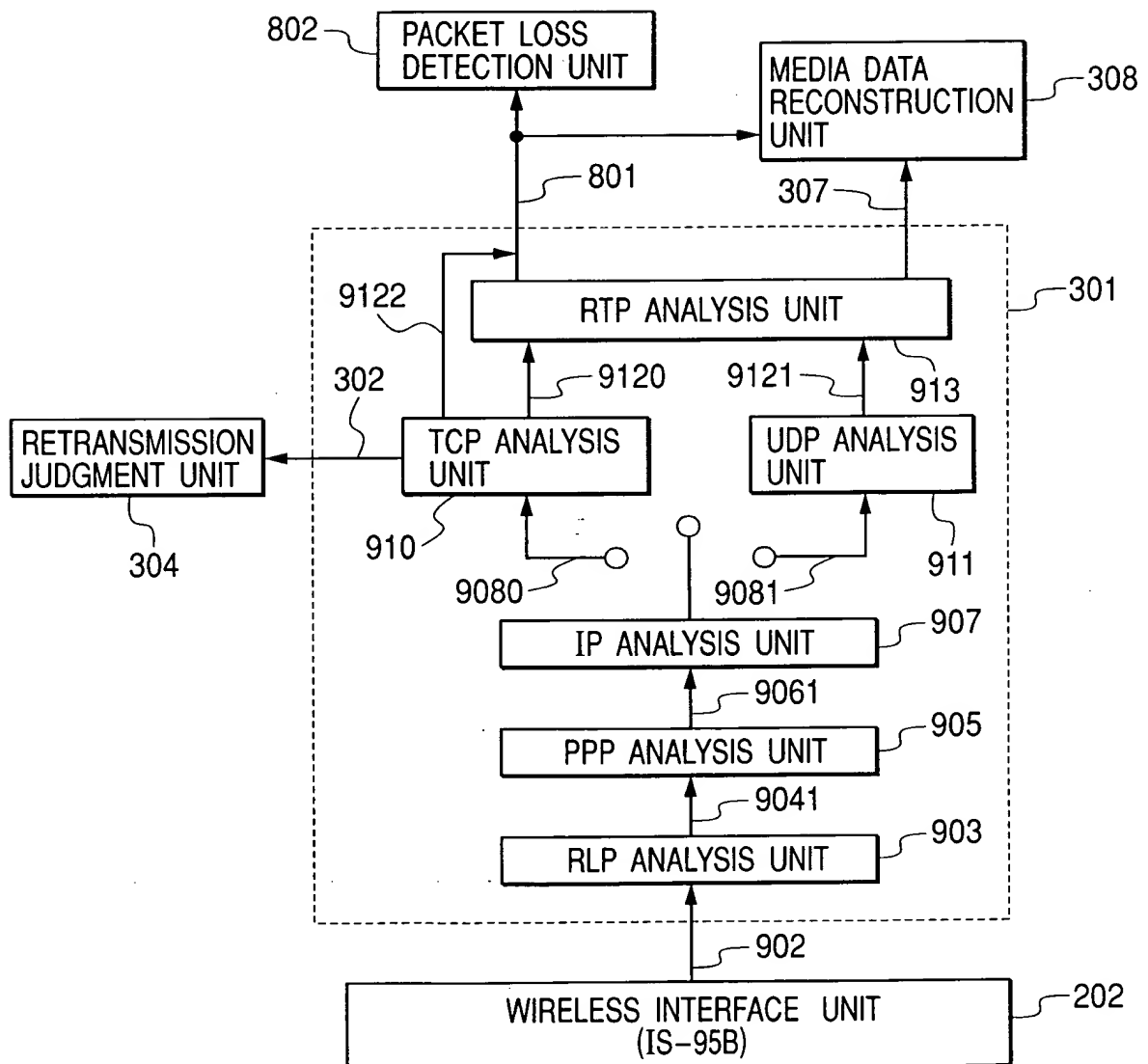


FIG. 43

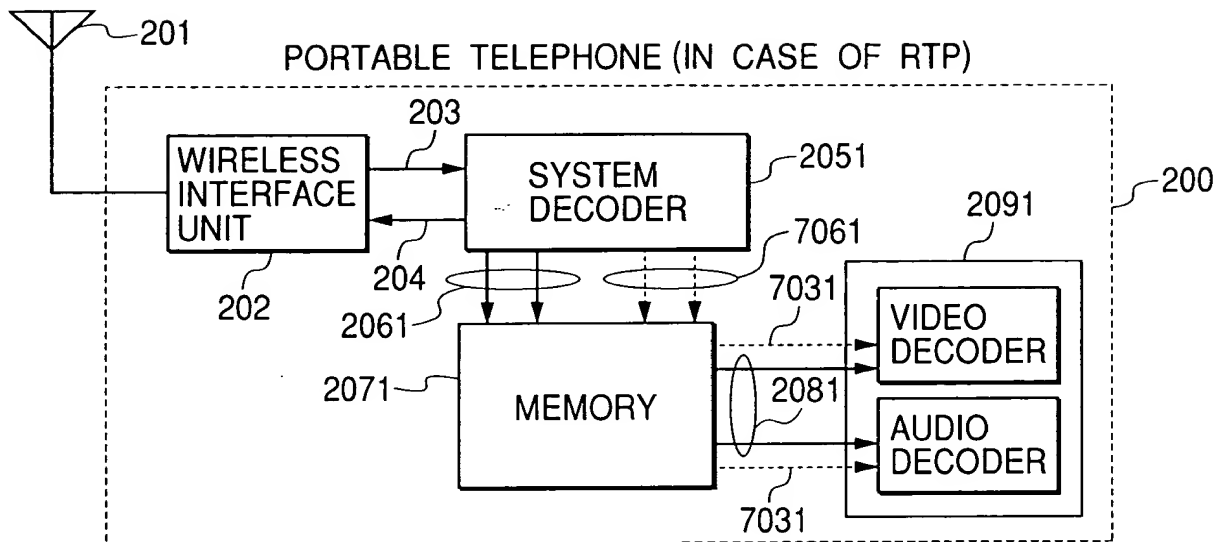


FIG. 44

